

Fig. 1(Prior Art)

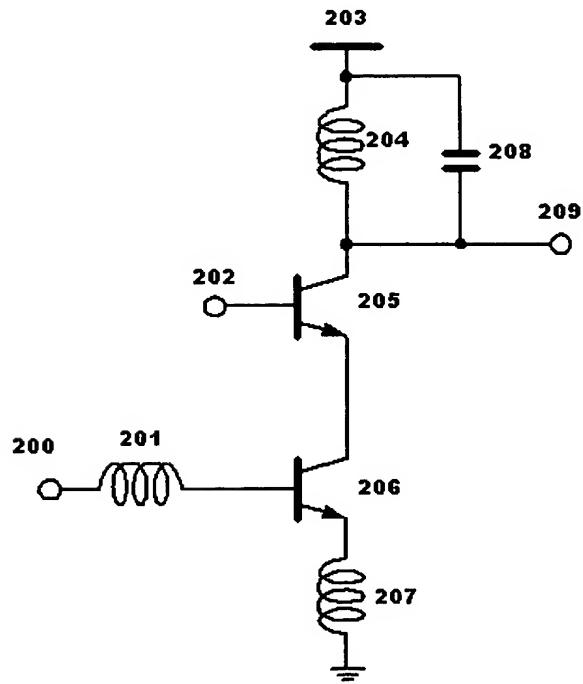


Fig. 2(Prior Art)

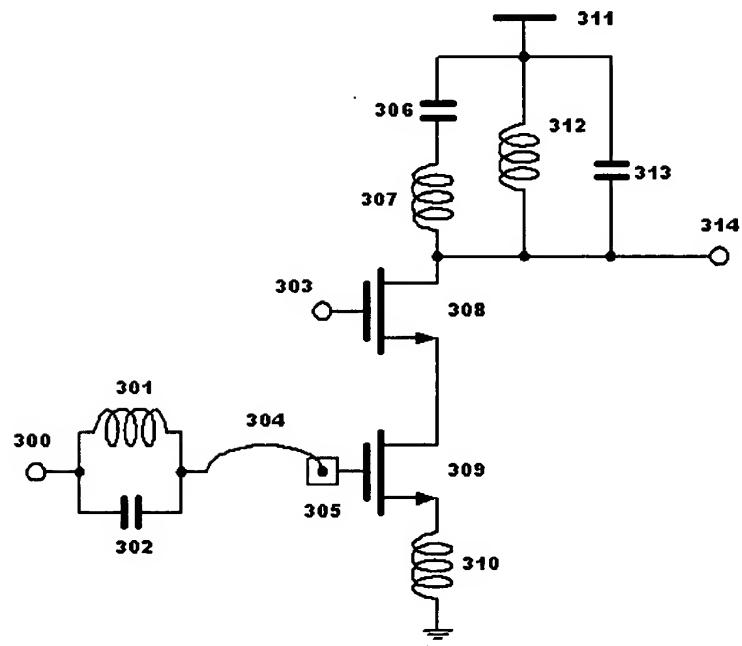


Fig. 3(Prior Art)

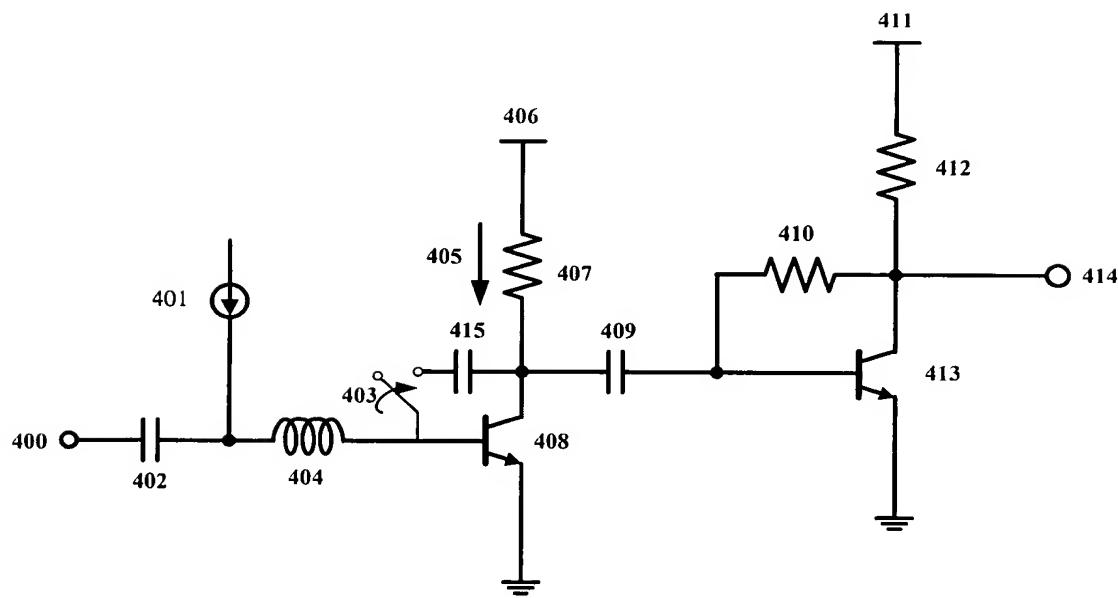


Fig. 4

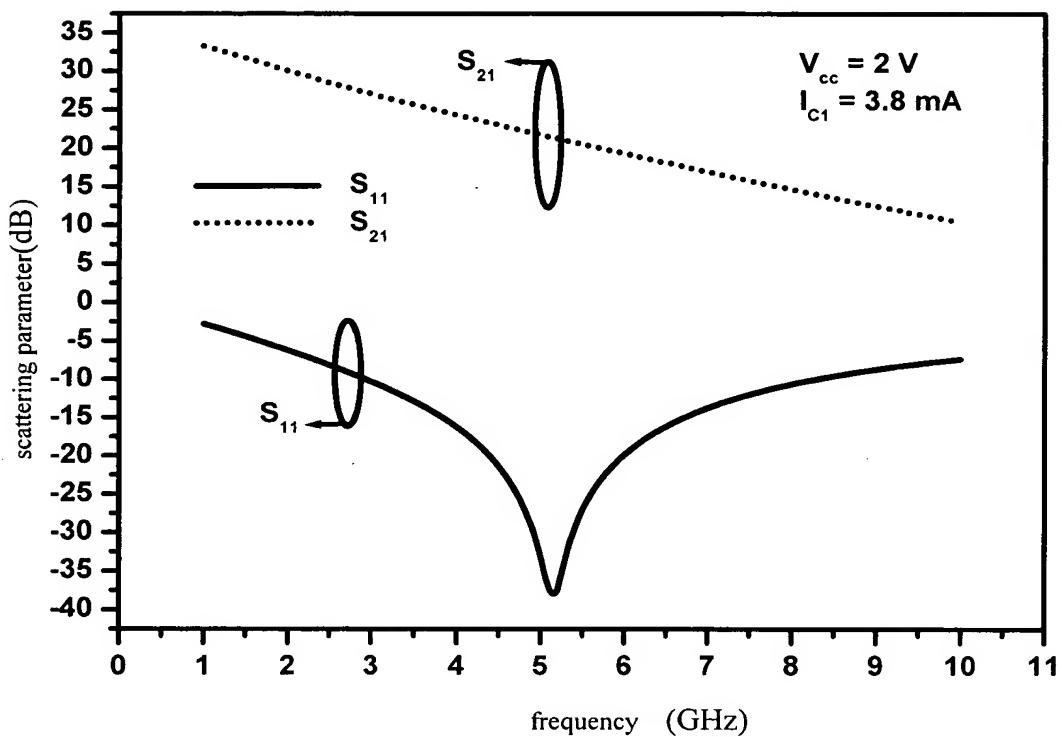


Fig. 5

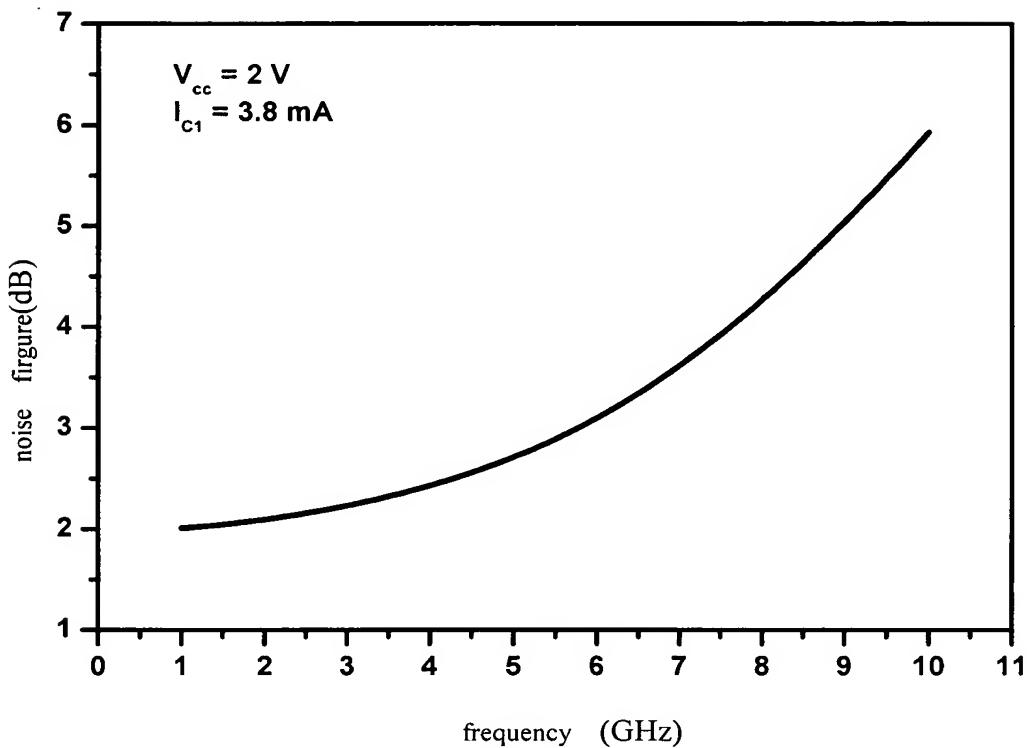


Fig. 6

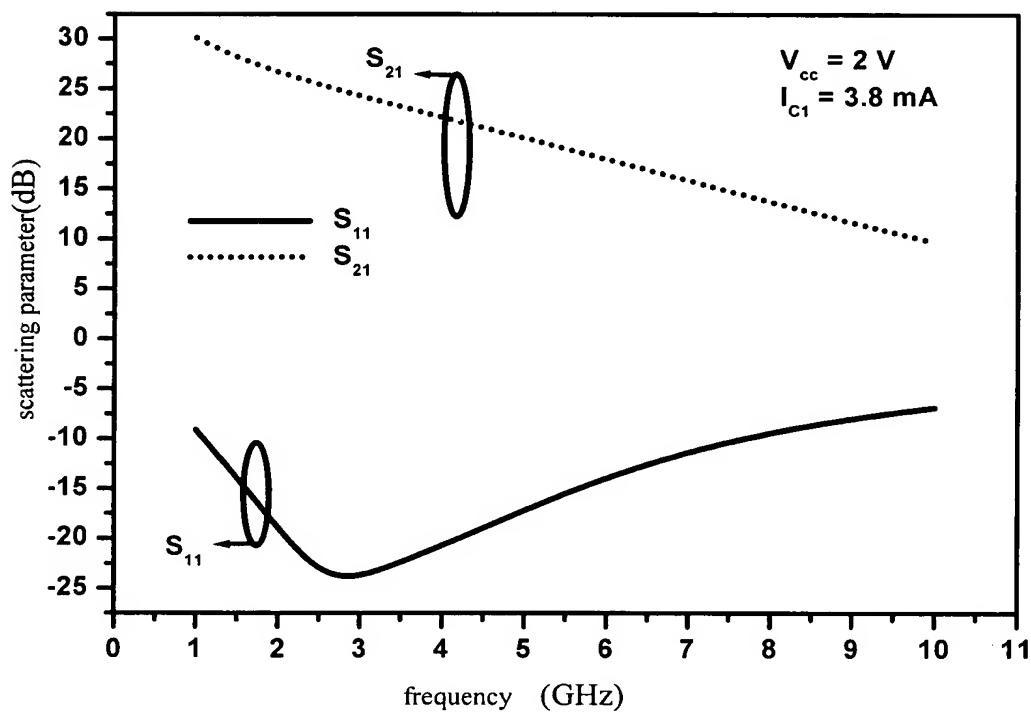


Fig. 7

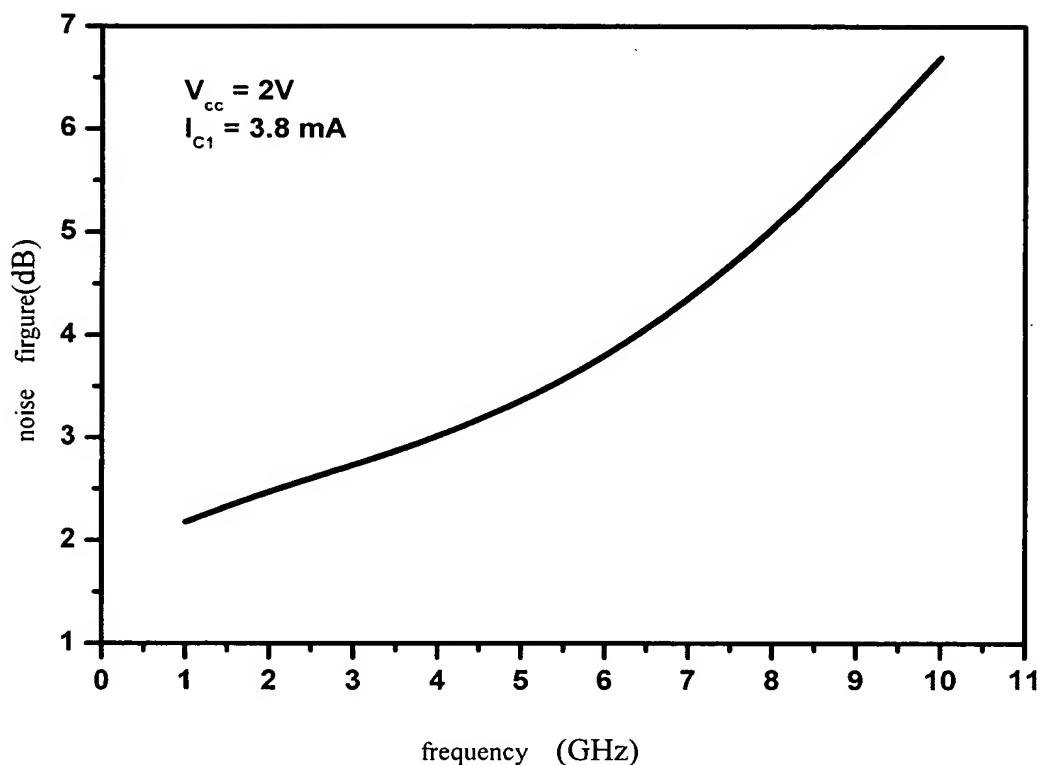


Fig. 8

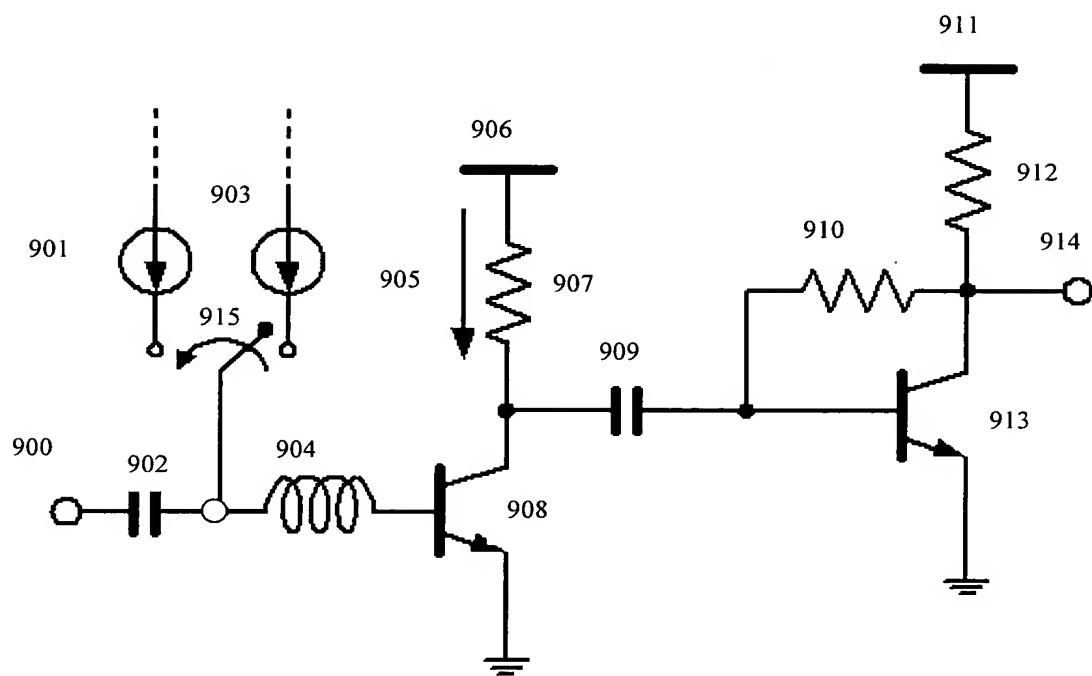


Fig. 9

Designing Methods and Circuits for Multi-Band Electronic Circuits
Inventors: Shey-shi Lu; Hung-Wei Chiu; Po-Wei Lee; Yu-Che Yang
Docket No.: DAF007 US

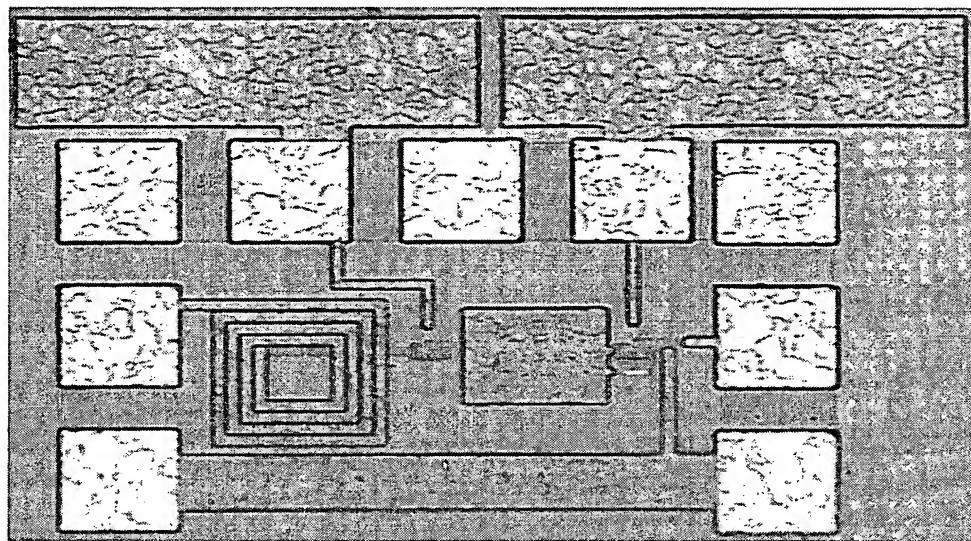


Fig. 10

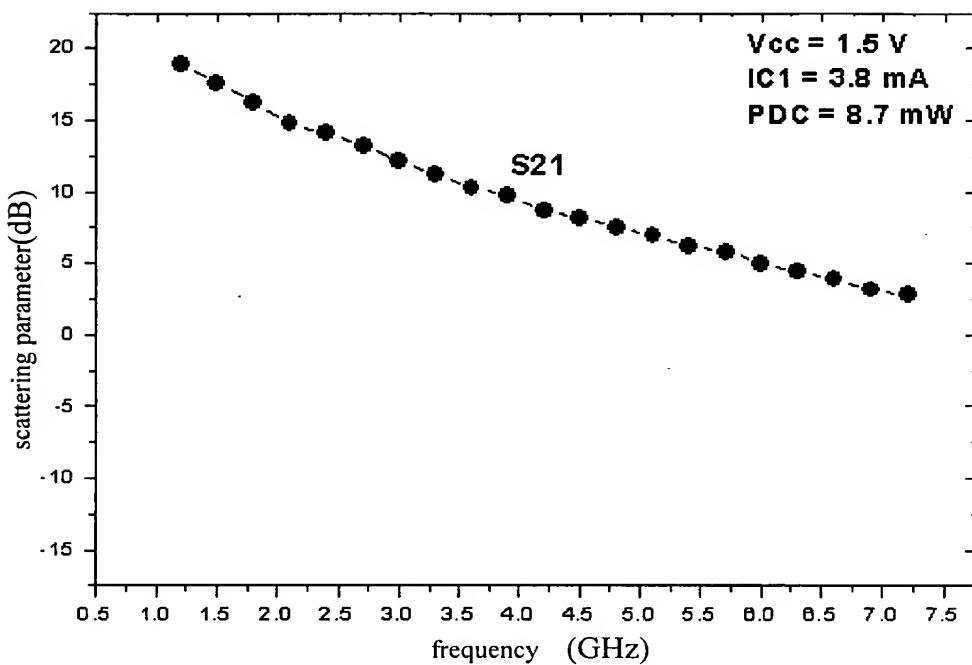


Fig. 11(a)

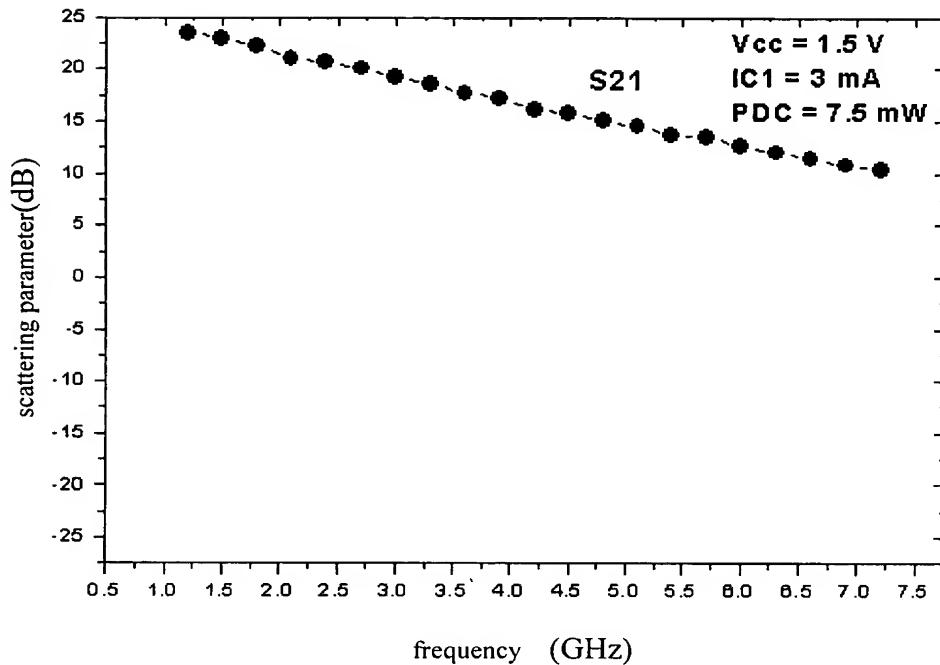


Fig. 11(b)

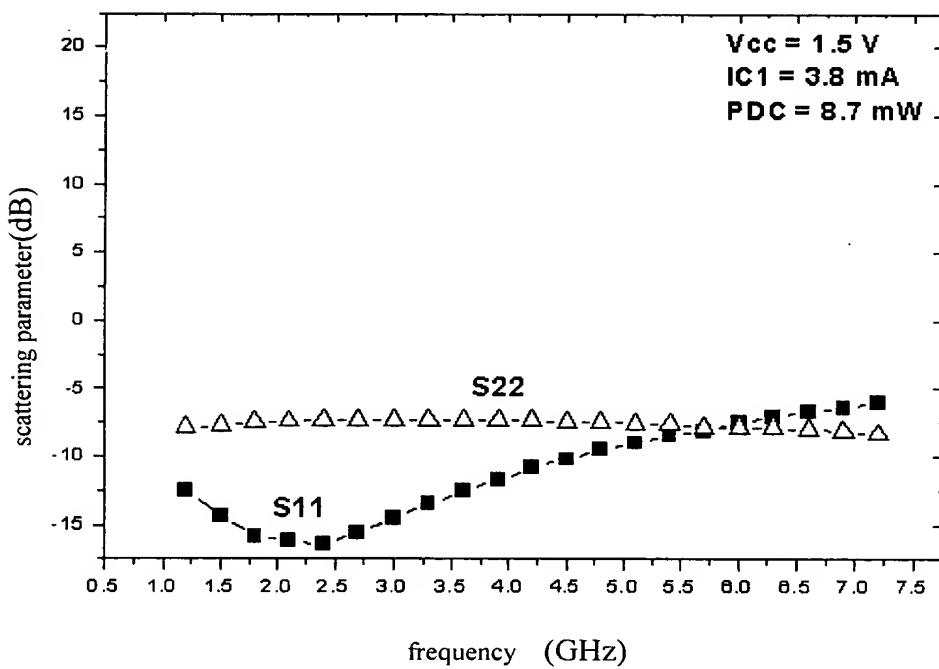


Fig.12(a)

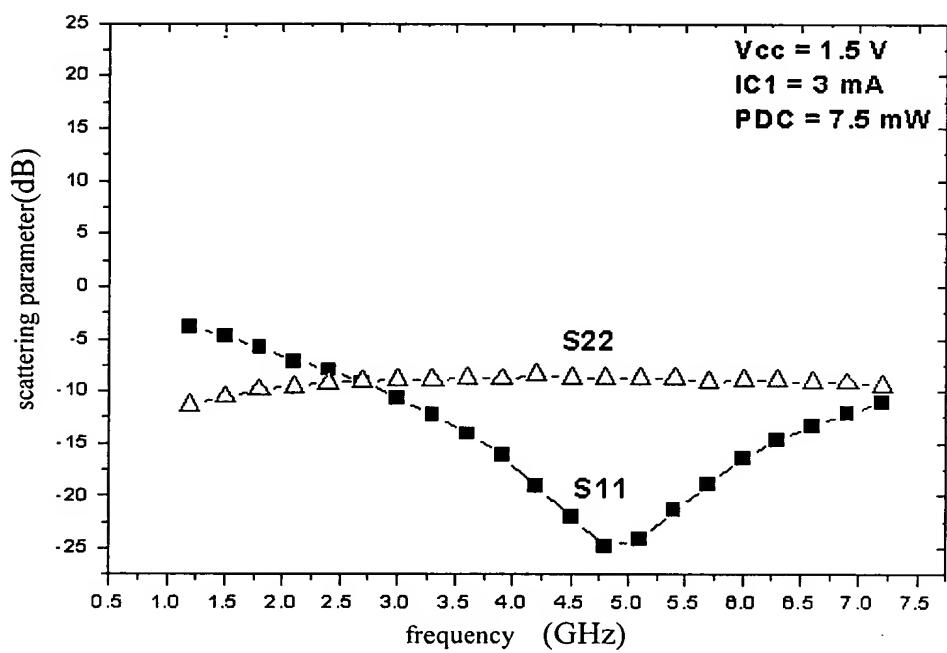


Fig. 12(b)

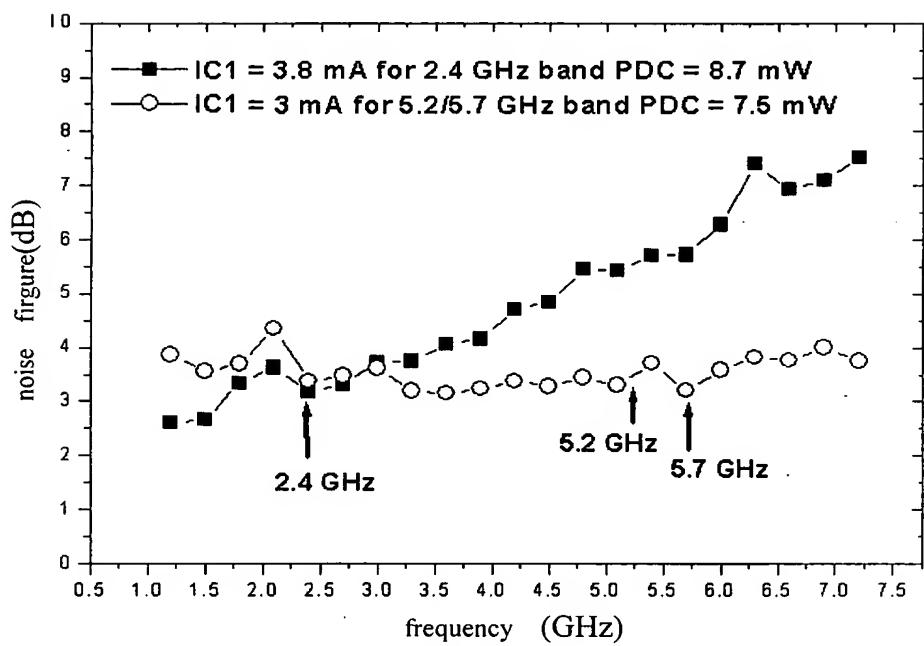


Fig. 13